

PTO/SB/91 (09-04)

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Application Number	10/734,357
Filing Date	December 12, 2003
First Named Inventor	Varadarajan Devnath
Title	Video Synchronization Signal Removal Circuitry
Art Unit	2817
Examiner Name	David C. Mis
Attorney Docket Number	11461.00.5763 (P05763)

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Applicant/Inventor.

Assignee of record of the entire interest. See 37 CFR 3.71.
Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96)

SIGNATURE of Applicant or Assignee of Record

Signature

Date

Name

Christopher J. Byrne

Telephone

408-721-6155

Title and Company

Director of IP & Technology Licensing; National Semiconductor Corporation

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below.



*Total of _____ forms are submitted.

This collection of information is required by 37 CFR 1.31 and 1.33. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 3 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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PTO/SB/86 (09-04)

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STATEMENT UNDER 37 CFR 3.73(b)Applicant/Patent Owner: Varadarajan Devnath, Jitendra Mohan, Quyet Nguyen and Yongseon KohApplication No./Patent No.: 10/734,357Filed/Issue Date: December 12, 2003Entitled: Inductive-Capacitive (LC) Based Quadrature Voltage Controlled Oscillator (VCO) With Deterministic Quadrature Signal Phase Relationship

National Semiconductor Corporation

a corporation

(Name of Assignee)

(Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. ☒ the assignee of the entire right, title, and interest; or
2. ☐ an assignee of less than the entire right, title and interest.
The extent (by percentage) of its ownership interest is _____ %

in the patent application/patent identified above by virtue of either:

A. ☒ An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel 014800, Frame 0773, or for which a copy thereof is attached.

OR

B. ☐ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:

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☐ Additional documents in the chain of title are listed on a supplemental sheet.

☐ Copies of assignments or other documents in the chain of title are attached.

[NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

Christopher J. Byrne

Signature

Printed or Typed Name

Director of IP & Technology Licensing, National Semiconductor

Title

Date

408-721-8155

Telephone Number

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